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FIG 1

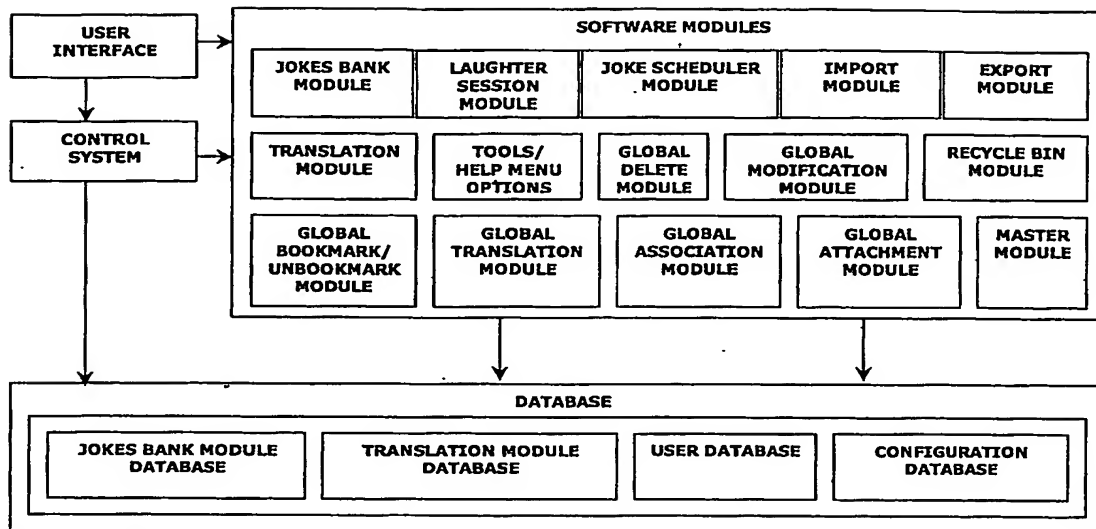
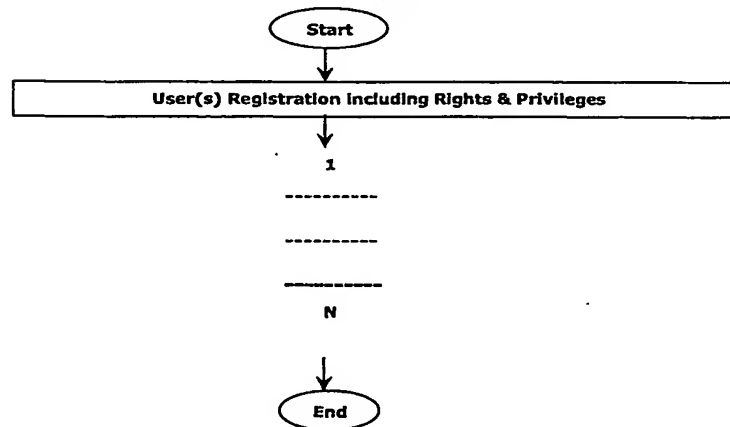
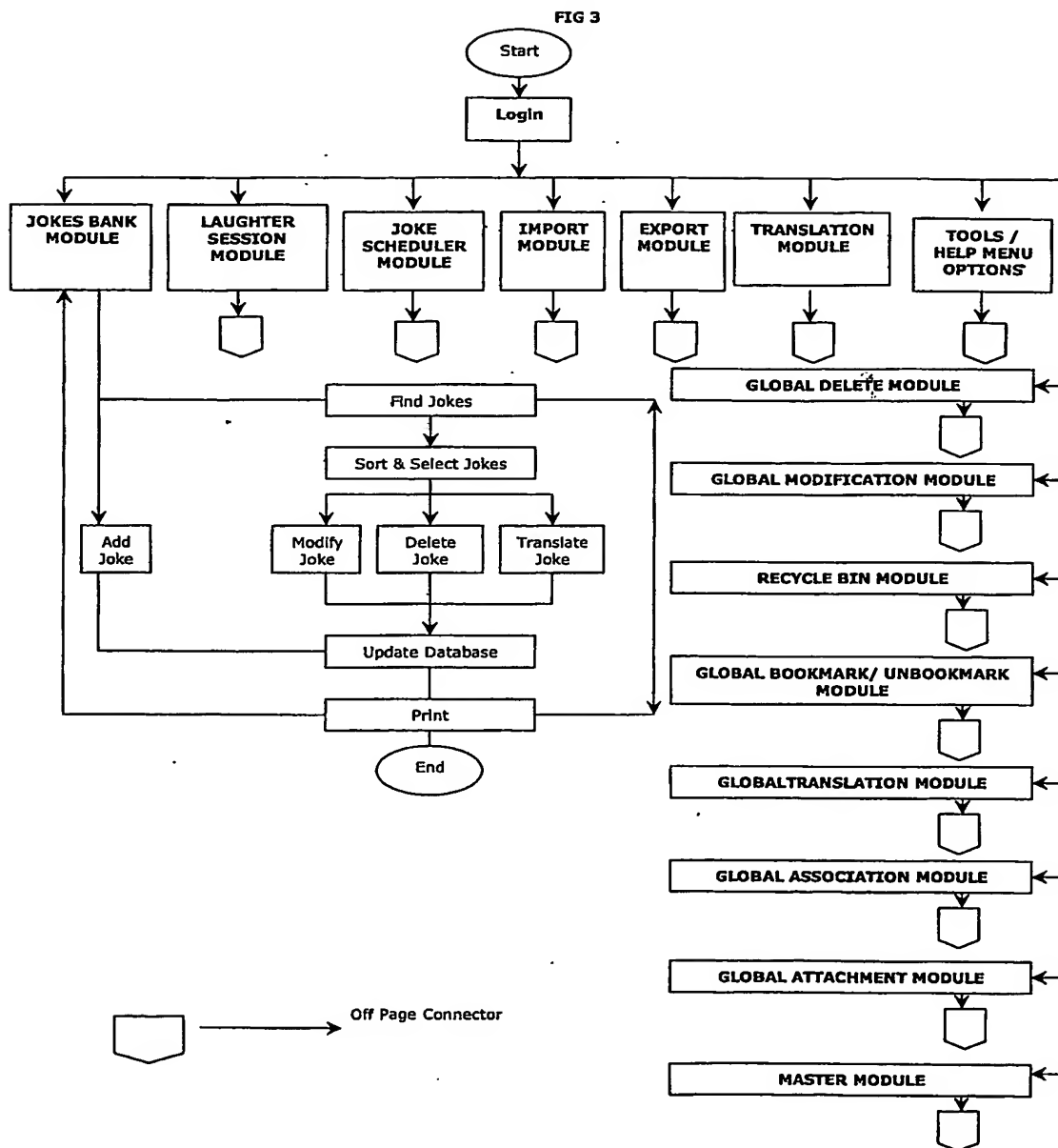


FIG 2

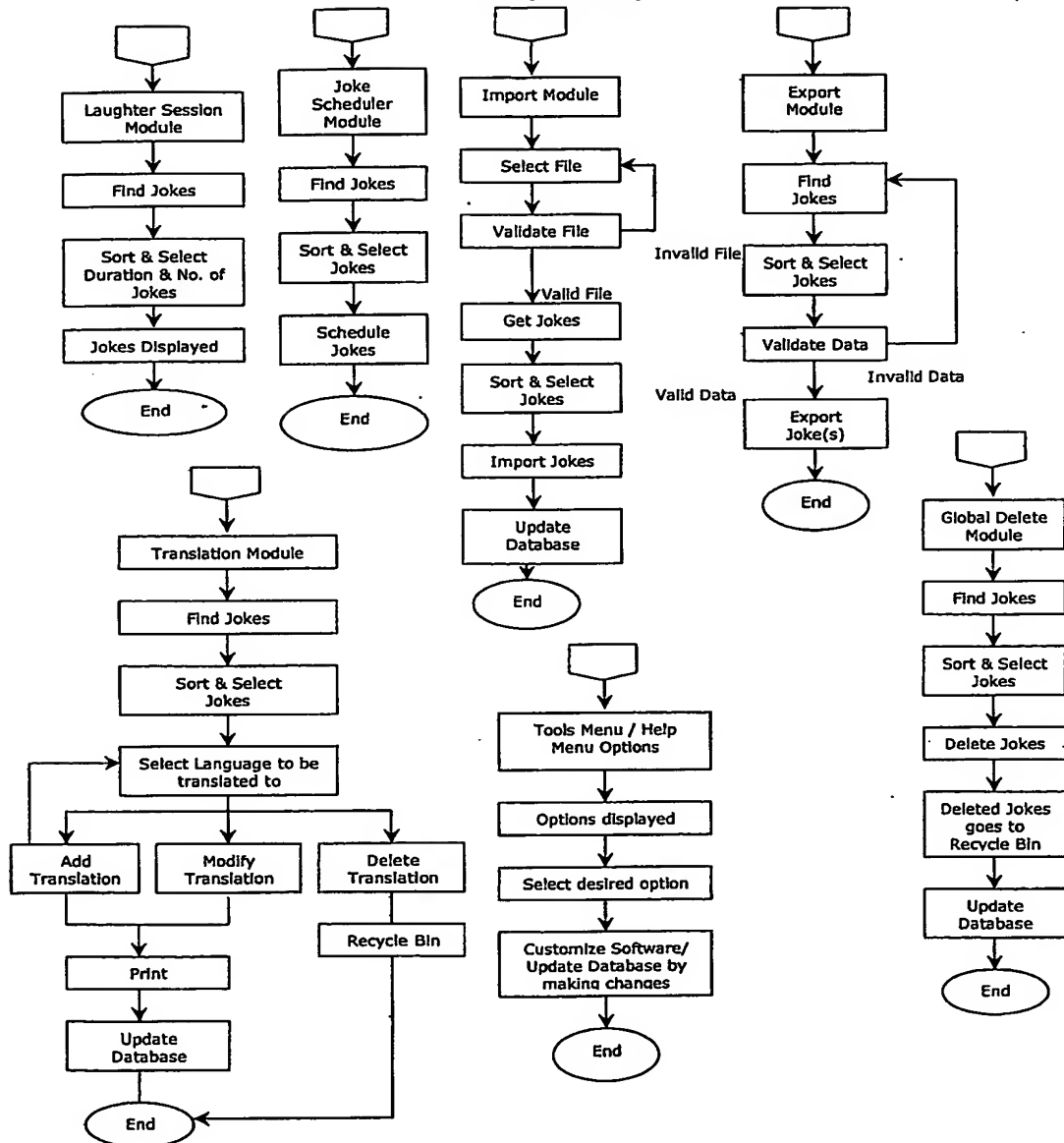


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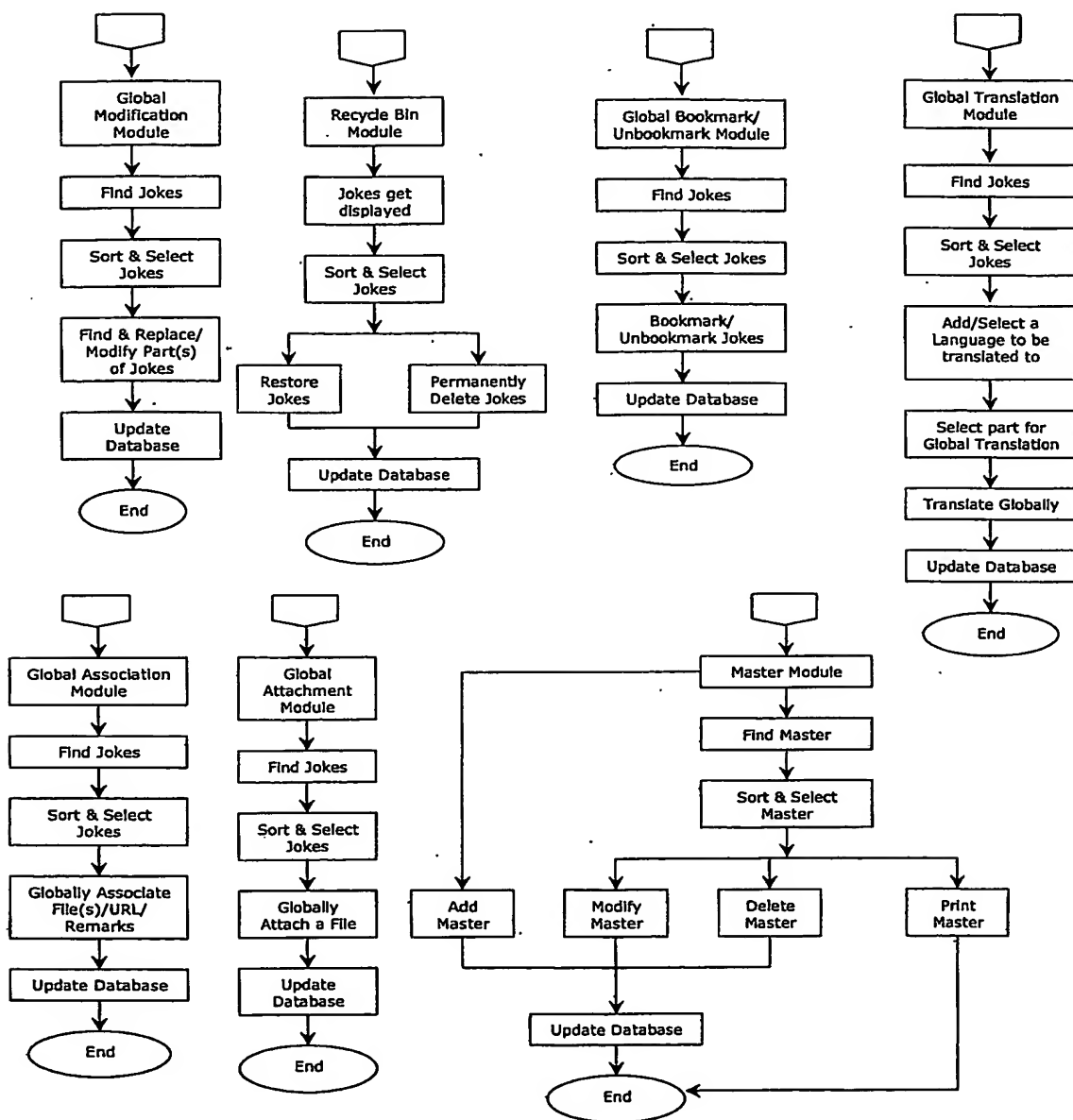
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FIG 3 (Continued)



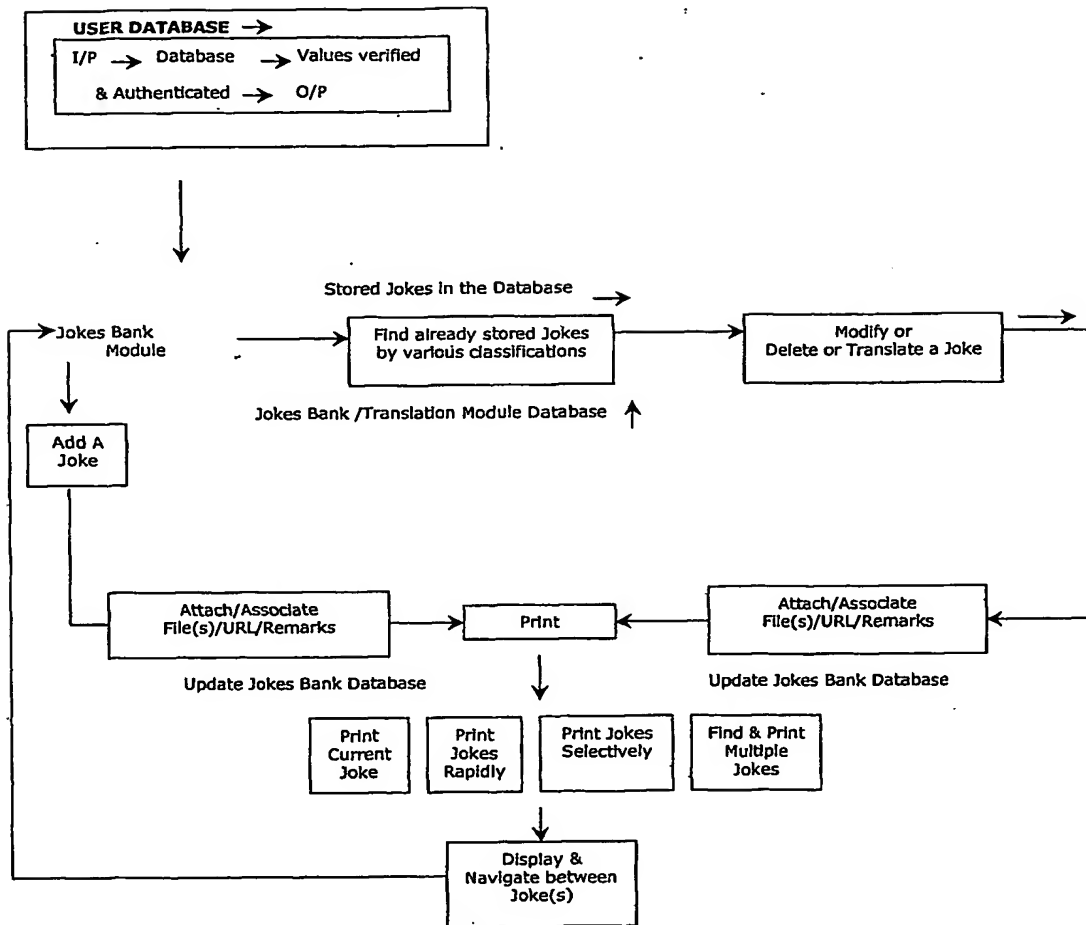
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FIG 3 (Continued)

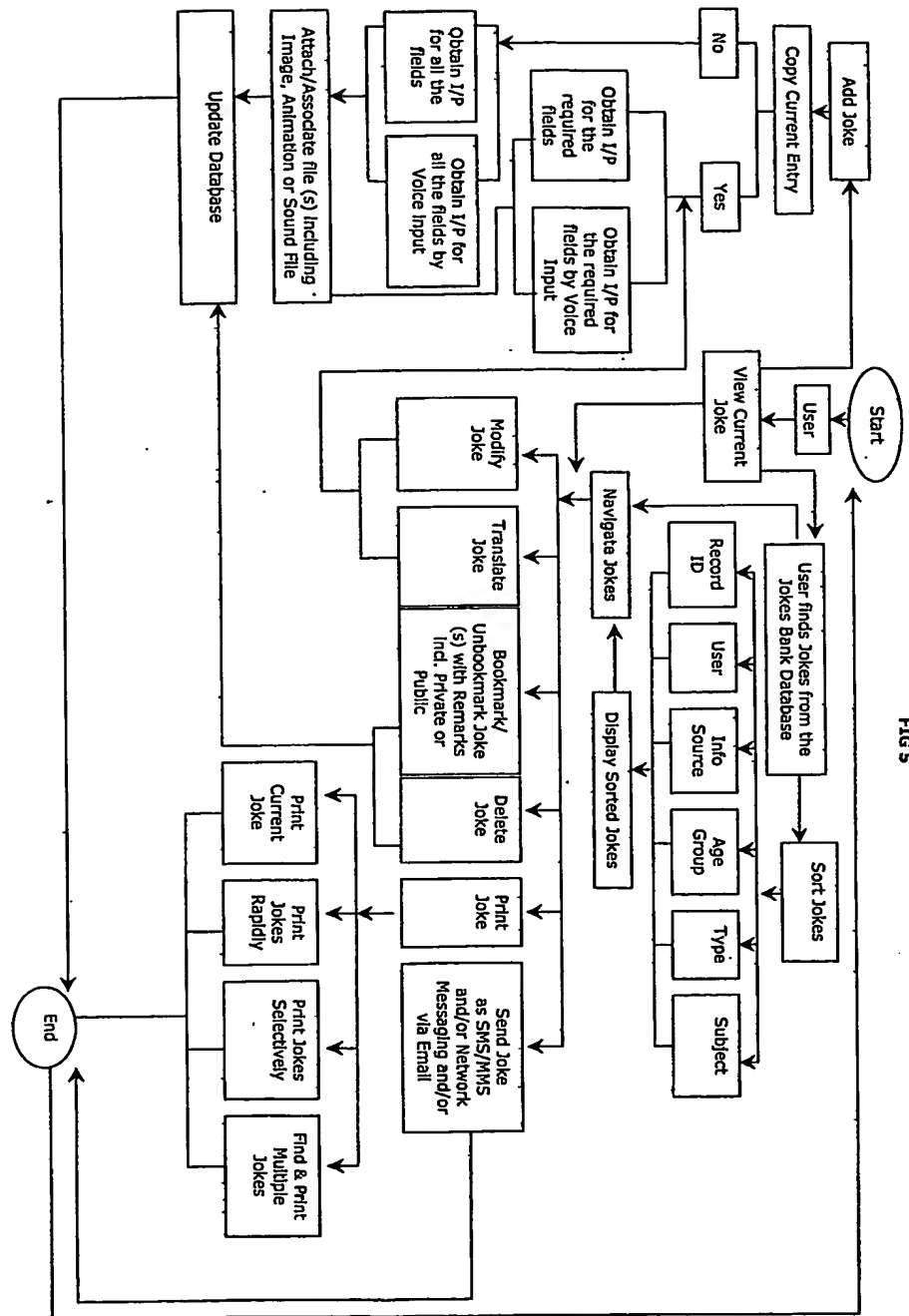


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FIG 4



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FIG 6

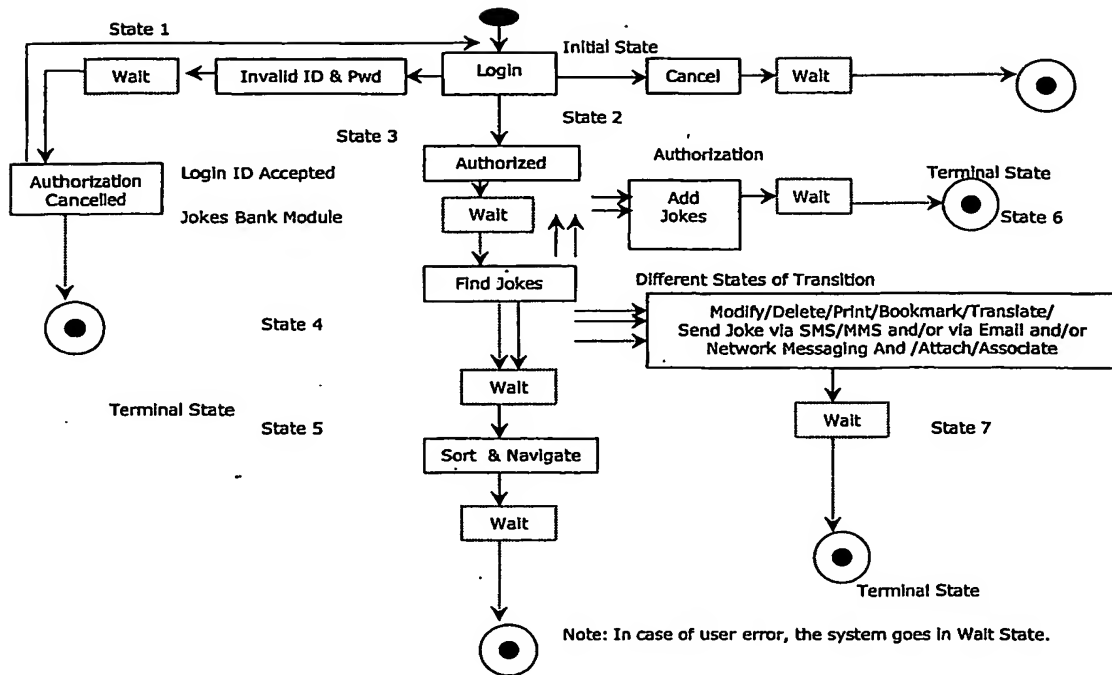
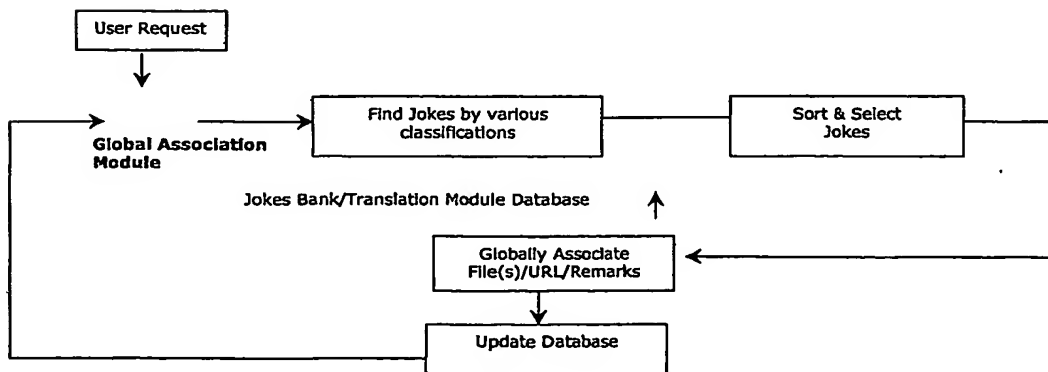
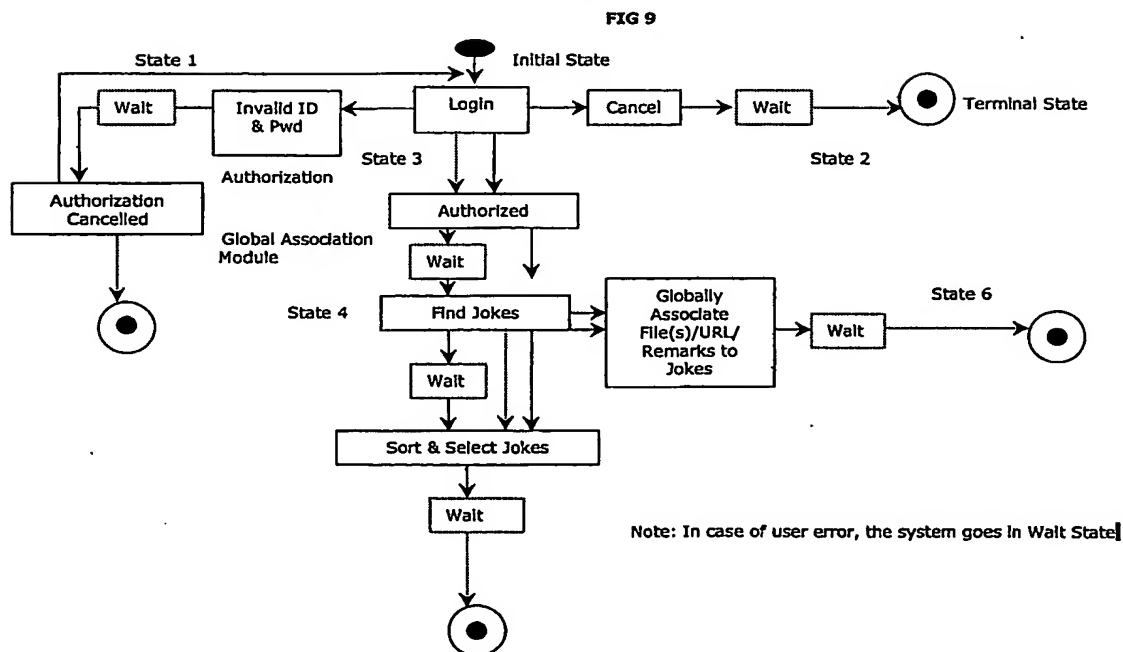
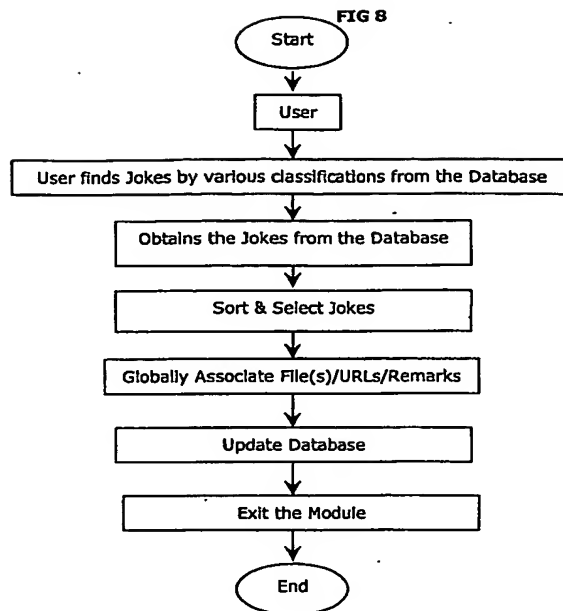


FIG 7



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FIG 10

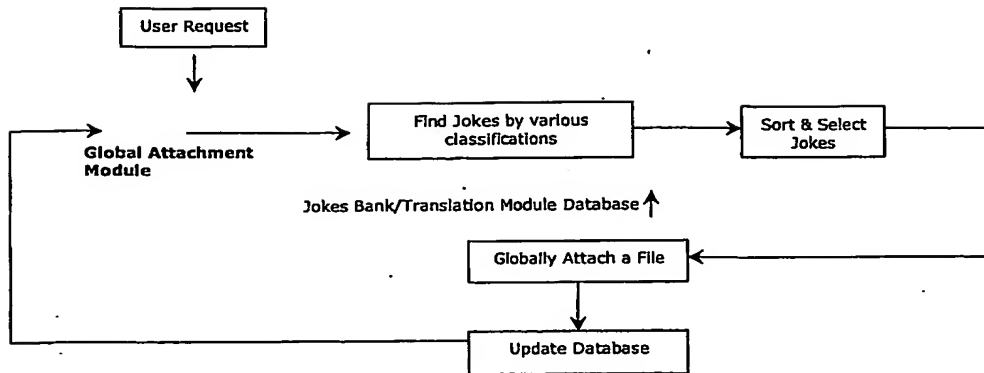
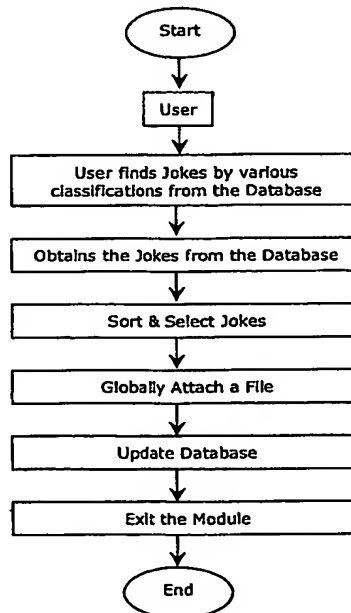
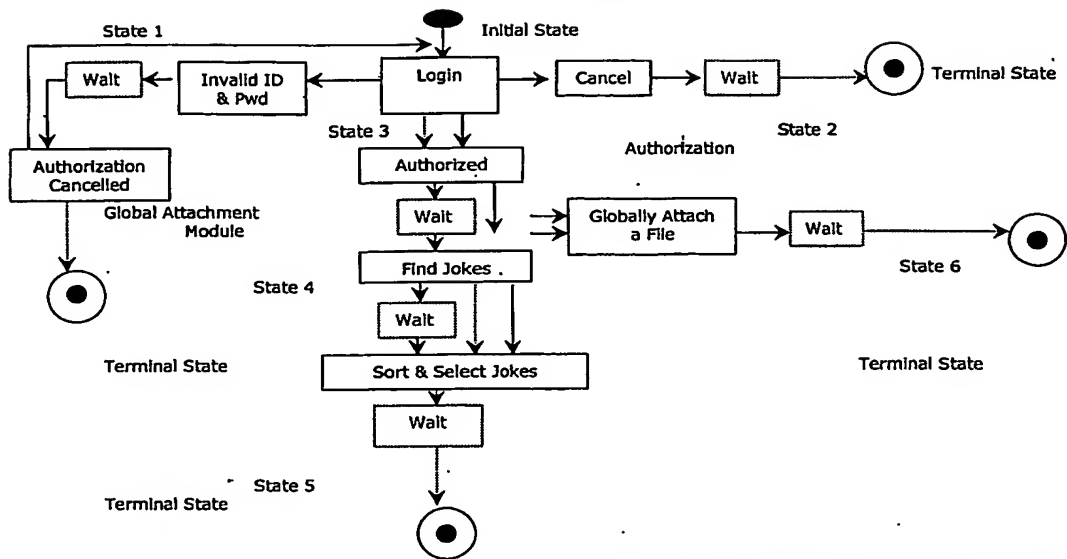


FIG 11



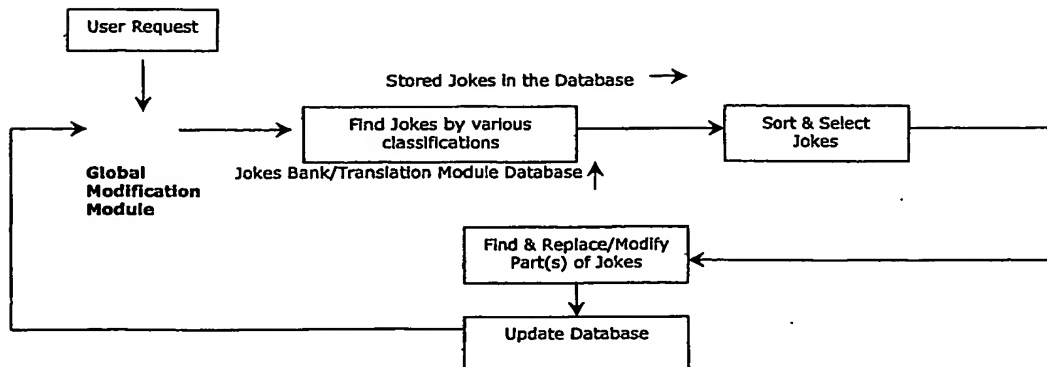
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FIG 12



Note: In case of user error, the system goes in Walt State.

FIG 13



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FIG 14

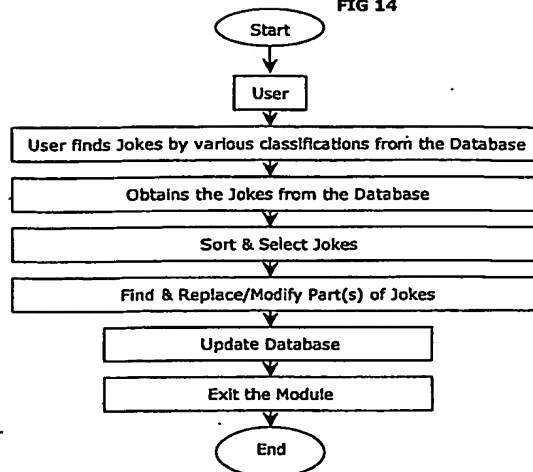
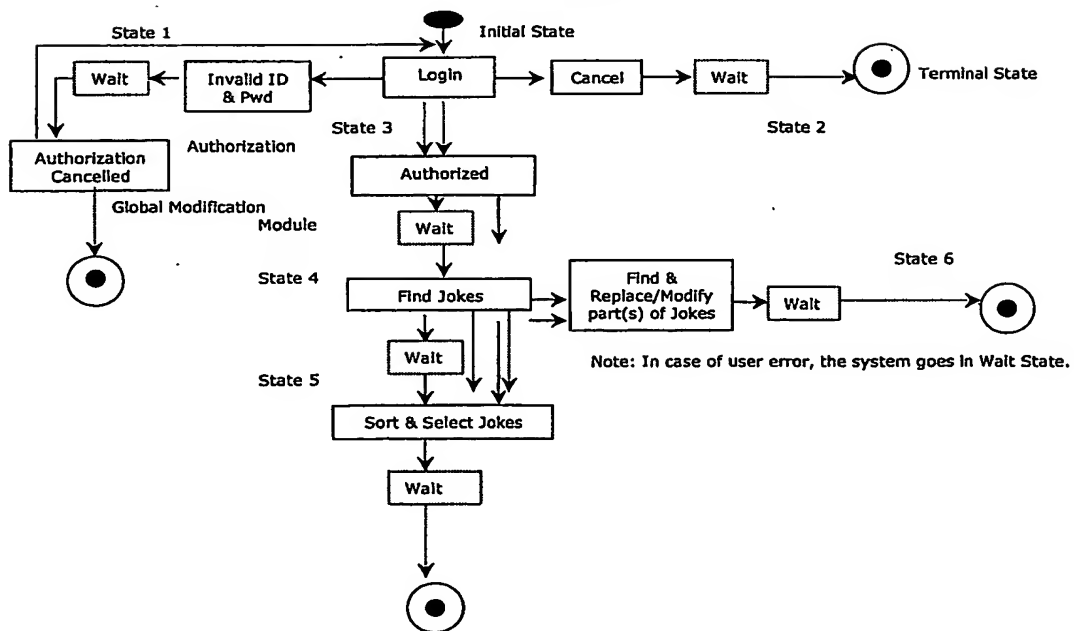


FIG 15



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FIG 16

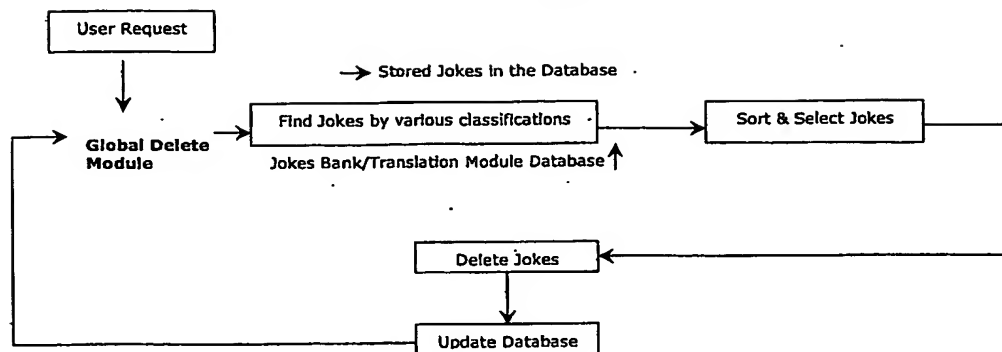
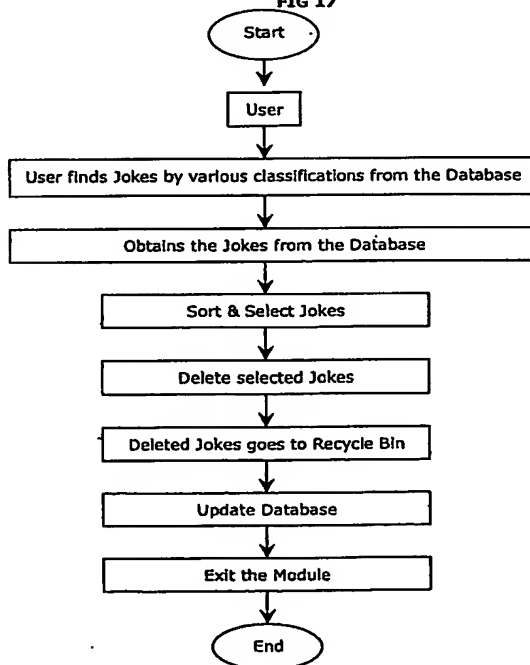
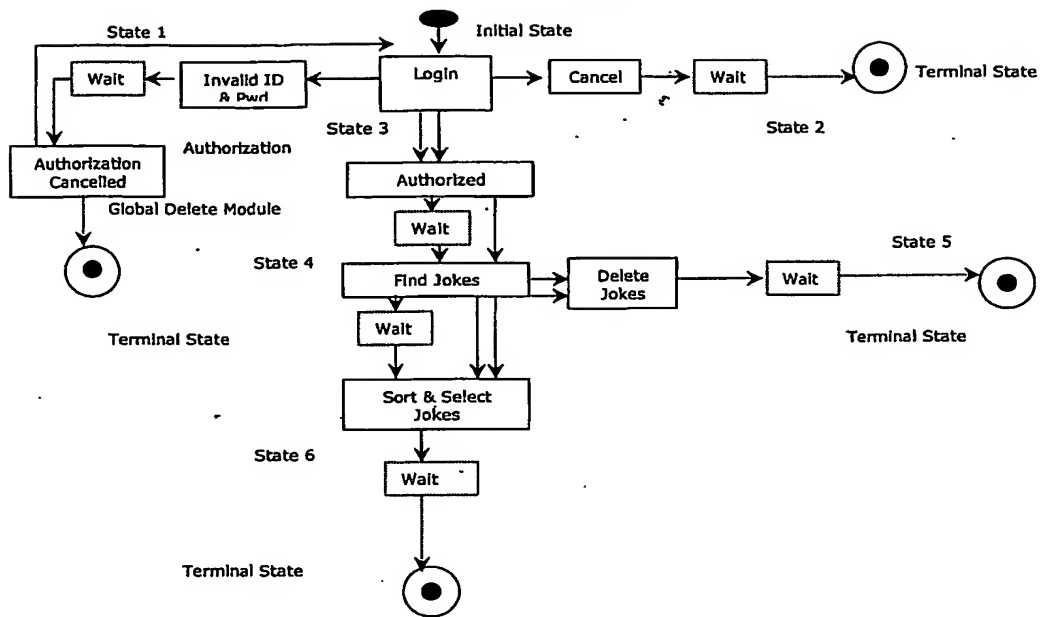


FIG 17



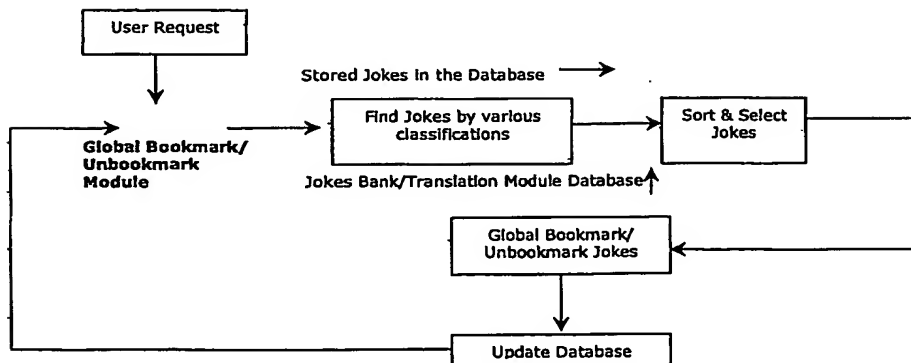
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FIG 18

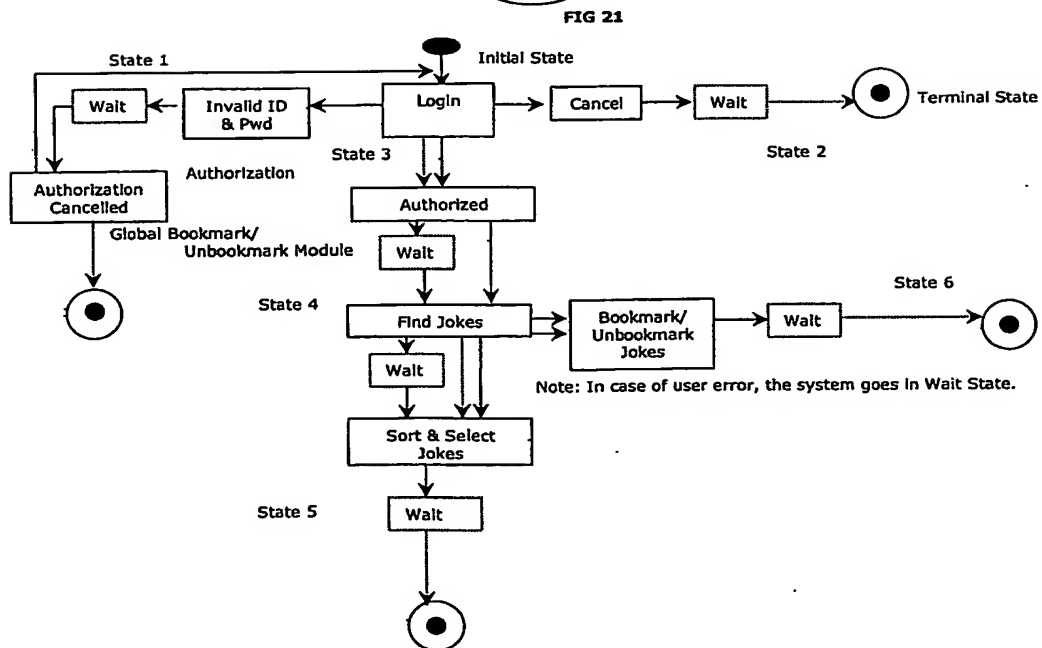
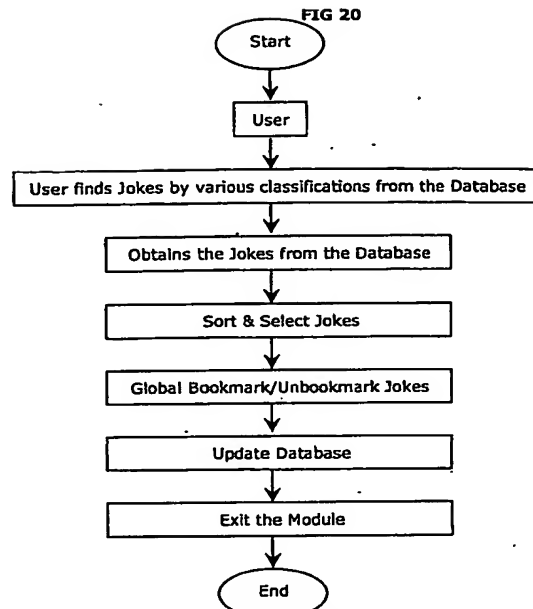


Note: In case of user error, the system goes in Wait State.

FIG 19



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FIG 22

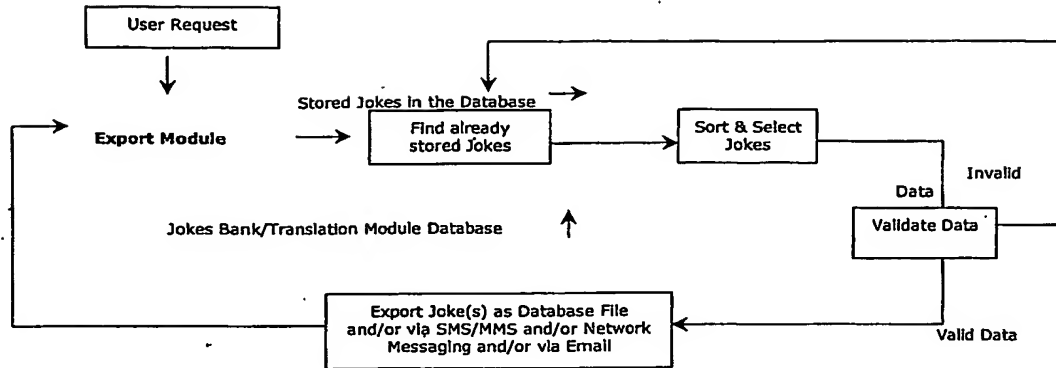
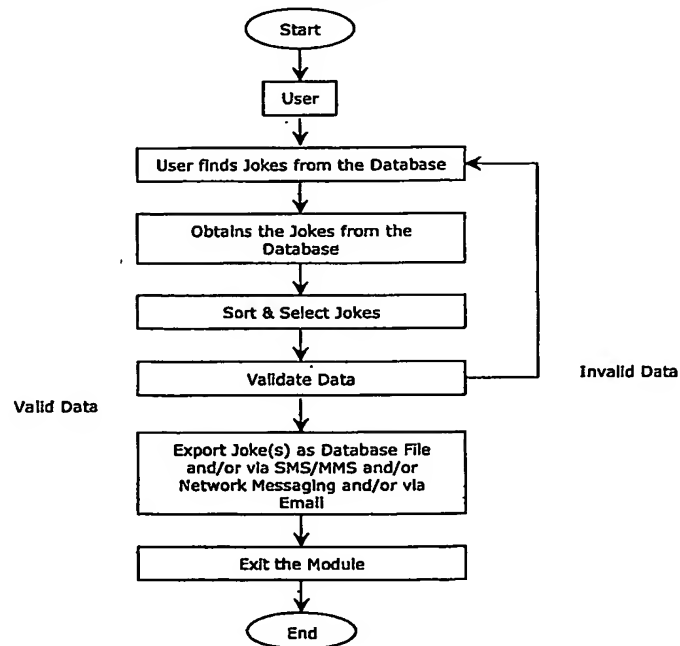
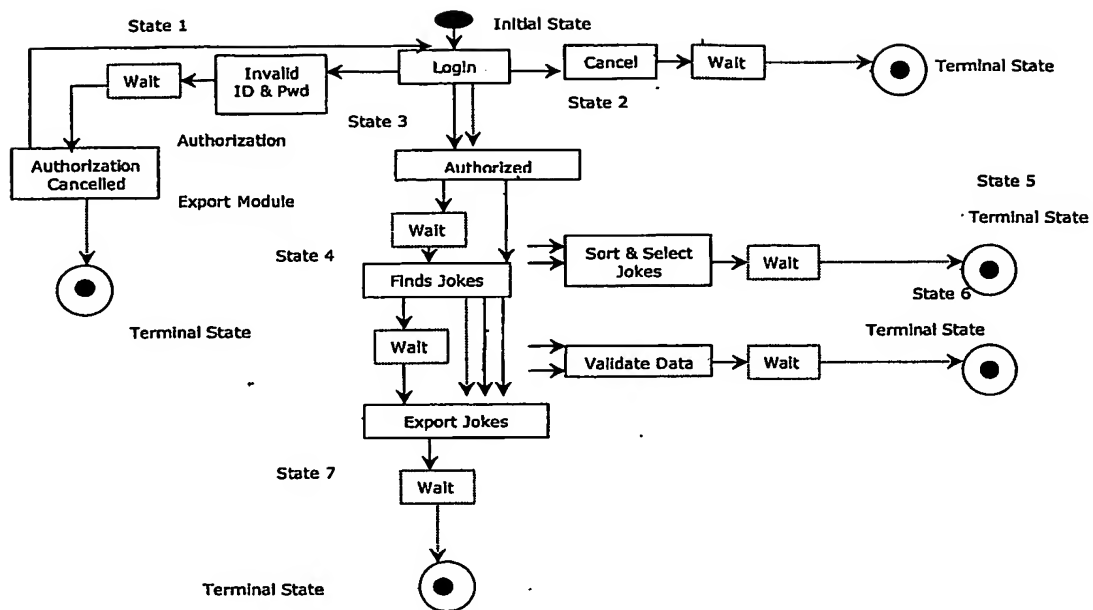


FIG 23



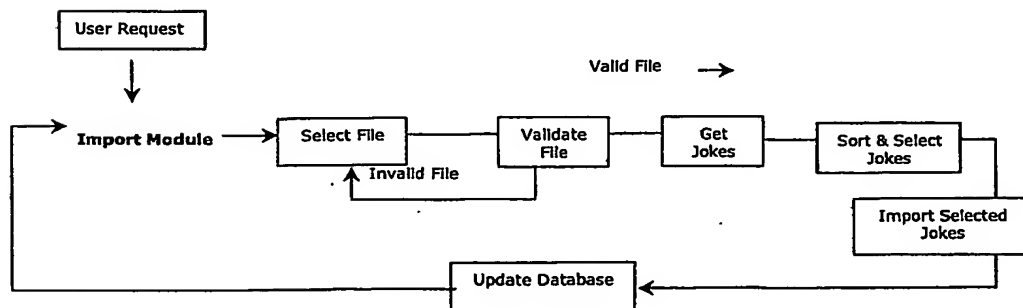
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FIG 24

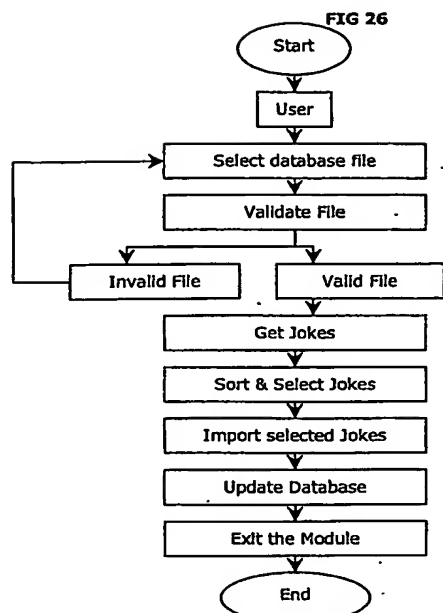
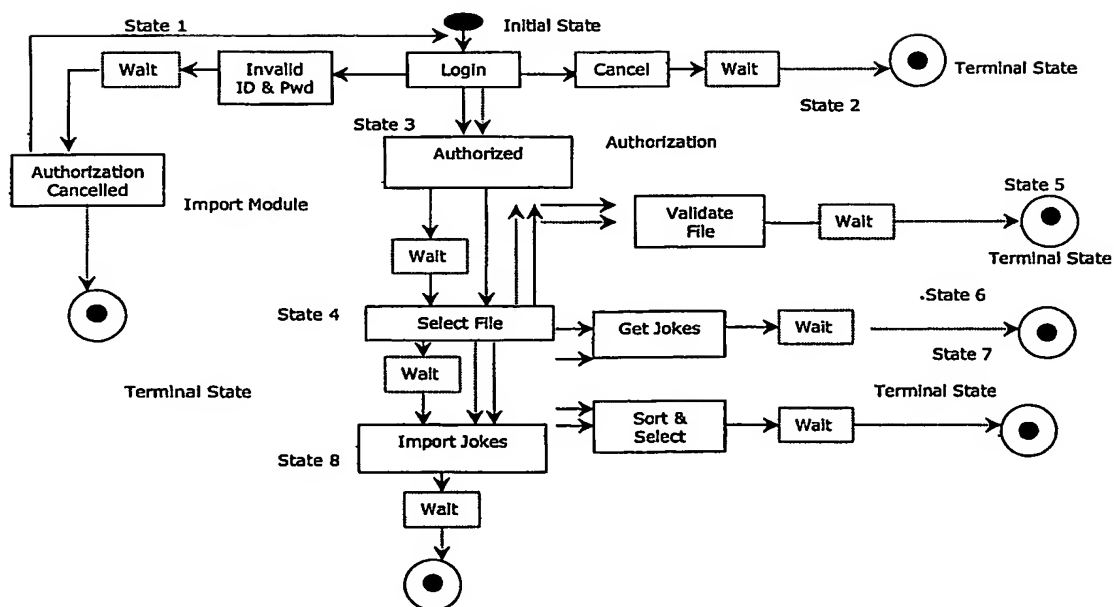


Note: In case of user error, the system goes in Wait State.

FIG 25



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**FIG 27**

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FIG 28

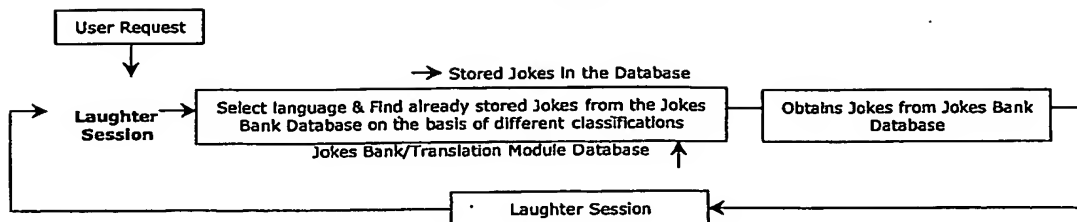
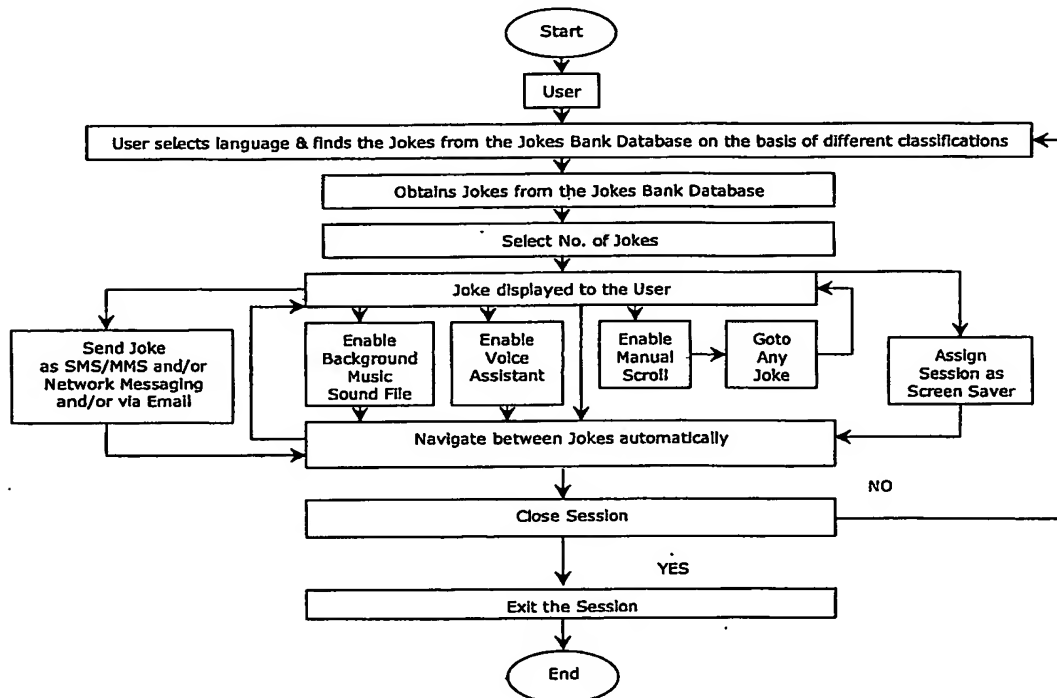


FIG 29



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FIG 30

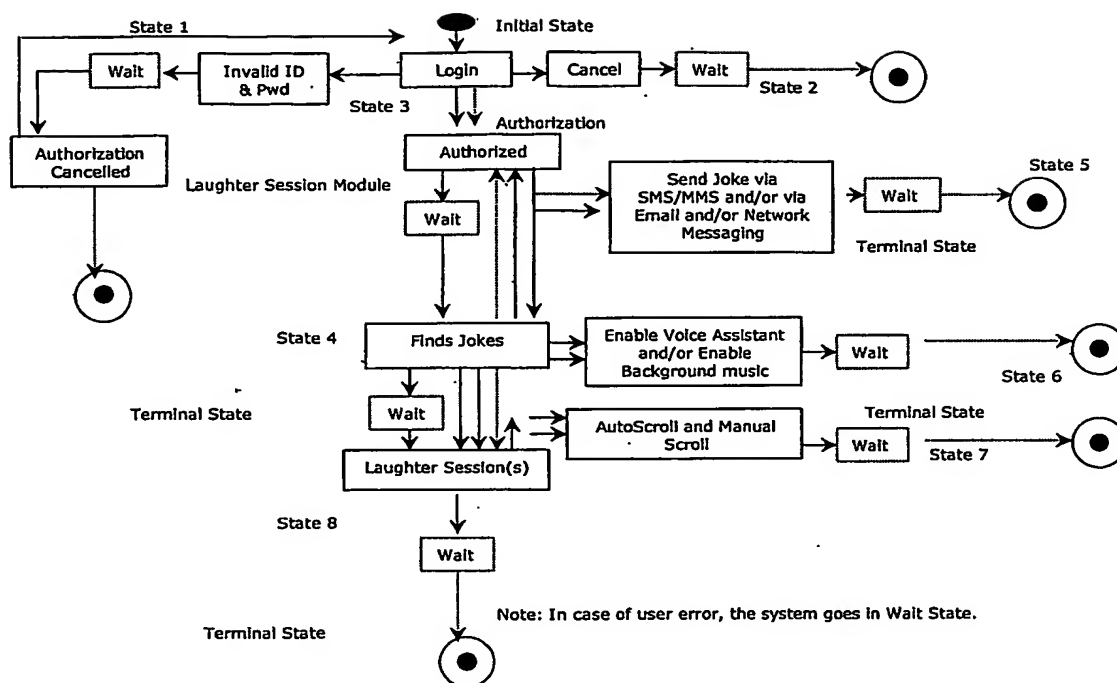
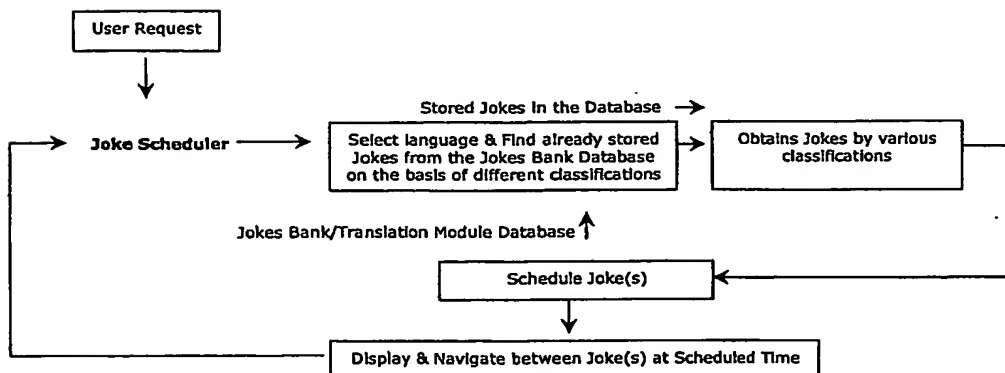
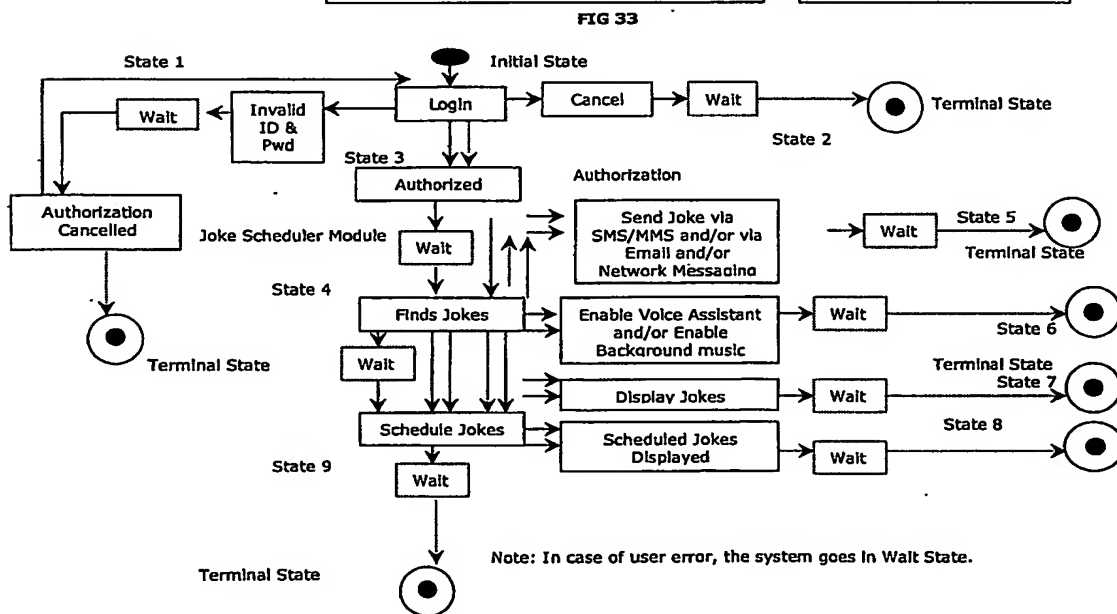
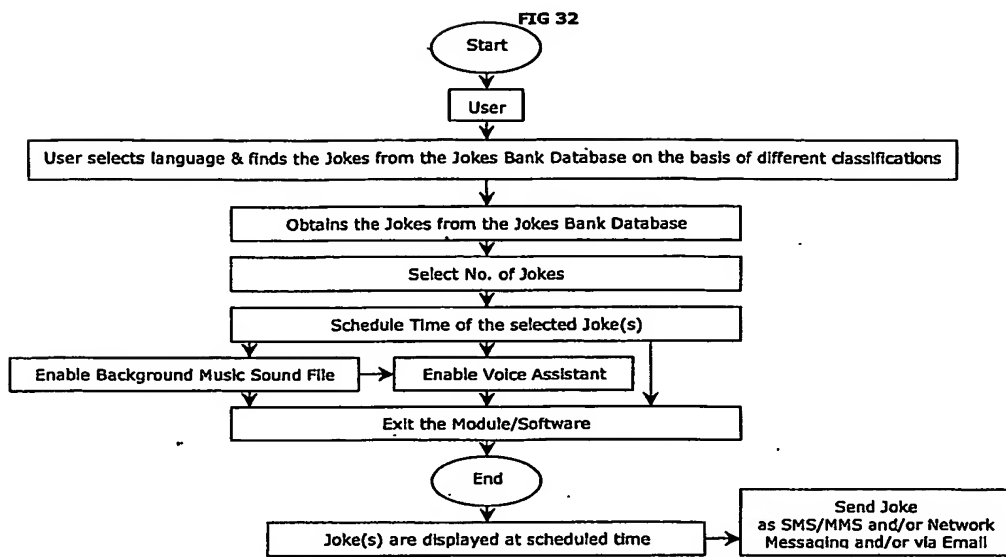


FIG 31



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FIG 34

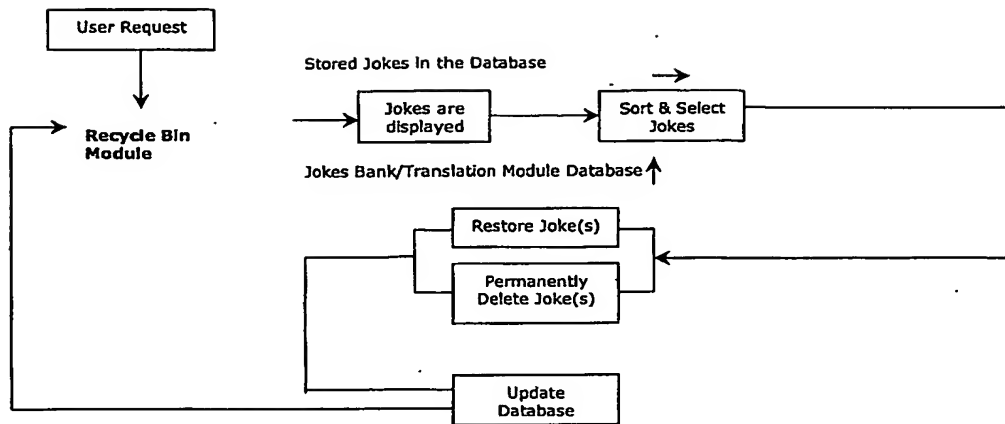
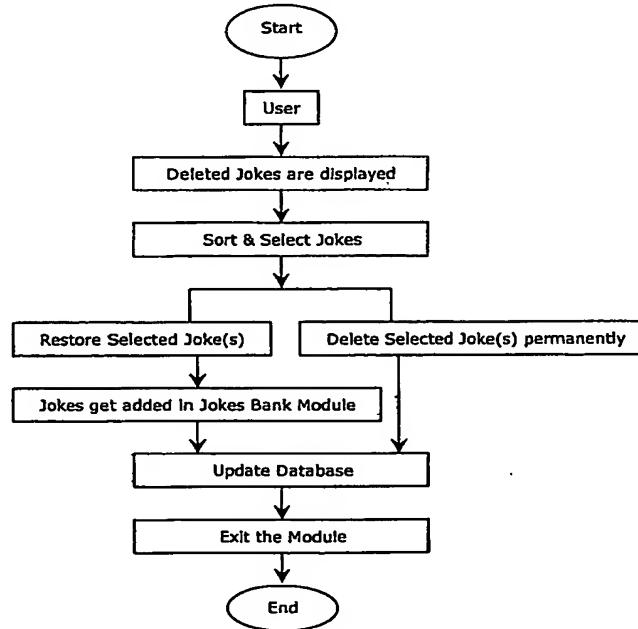


FIG 35



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FIG 36

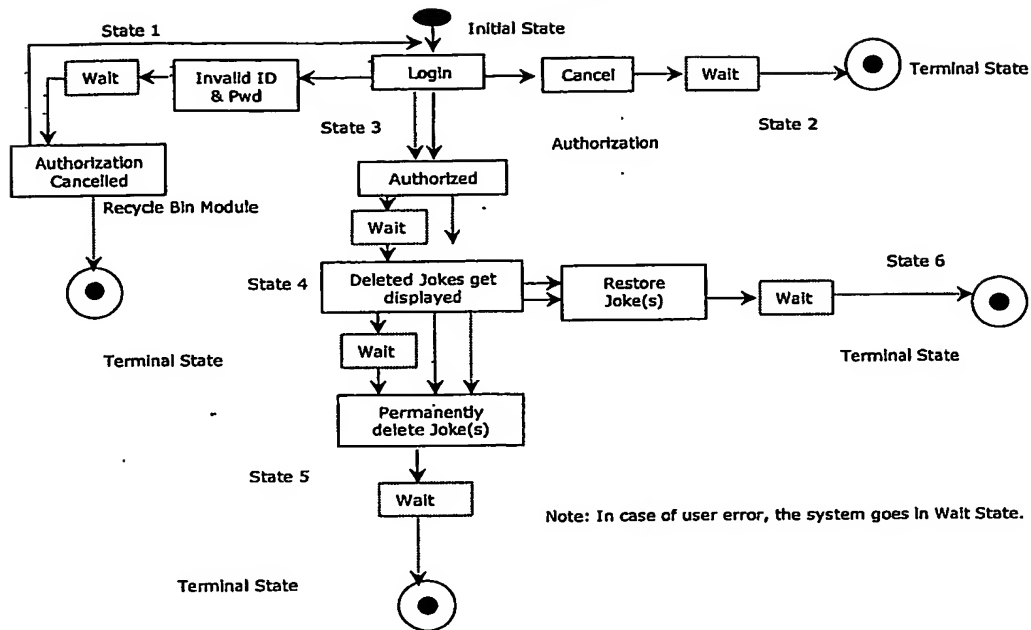
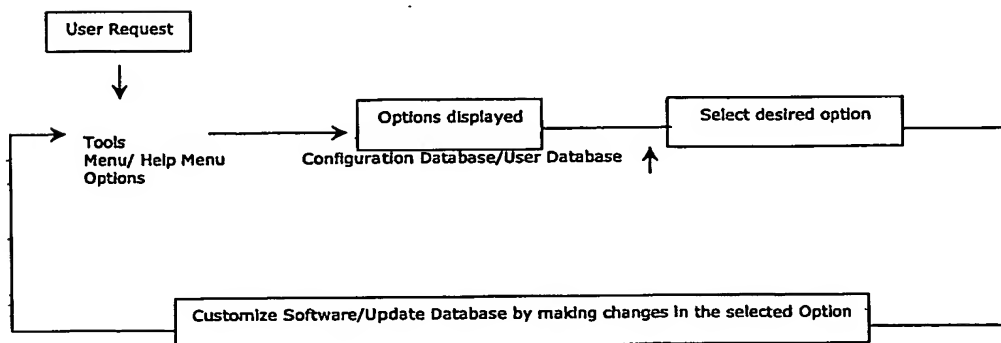


FIG 37



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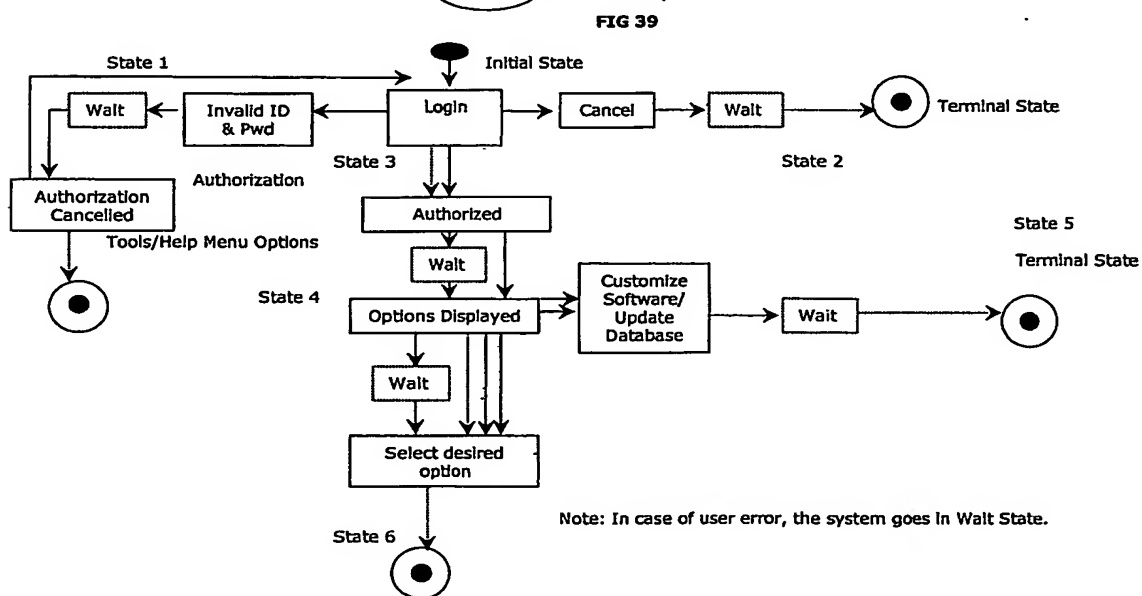
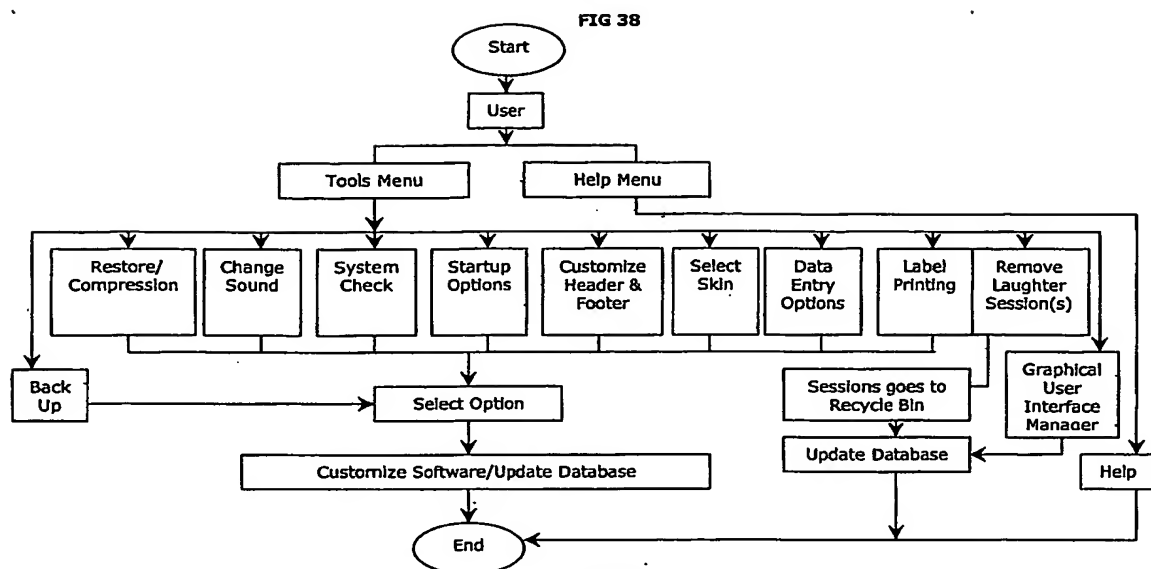


FIG 40

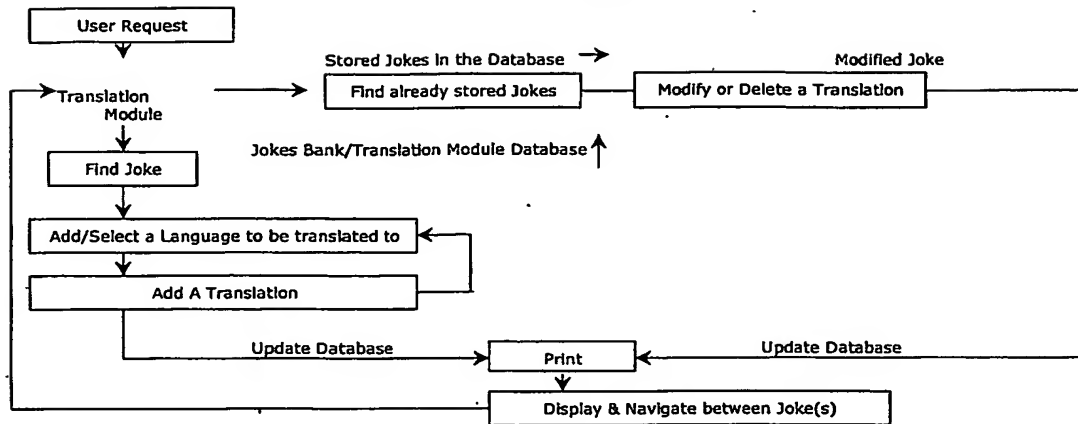
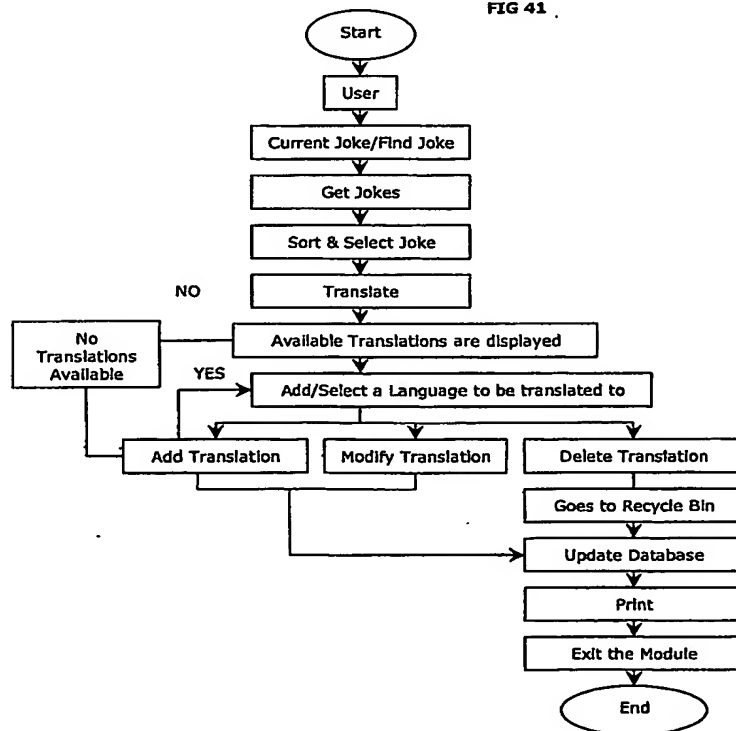


FIG 41



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FIG 42

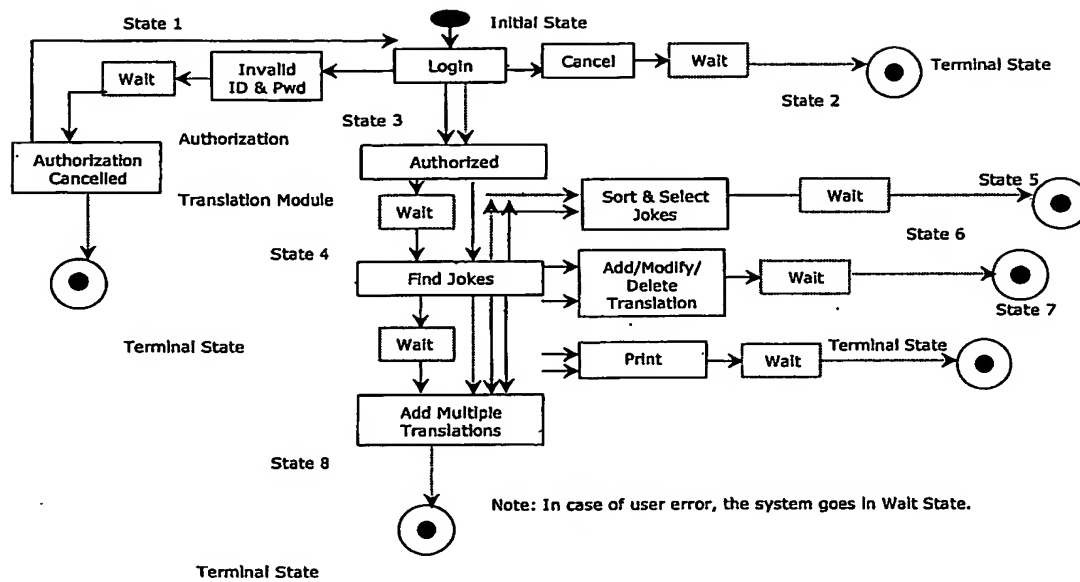
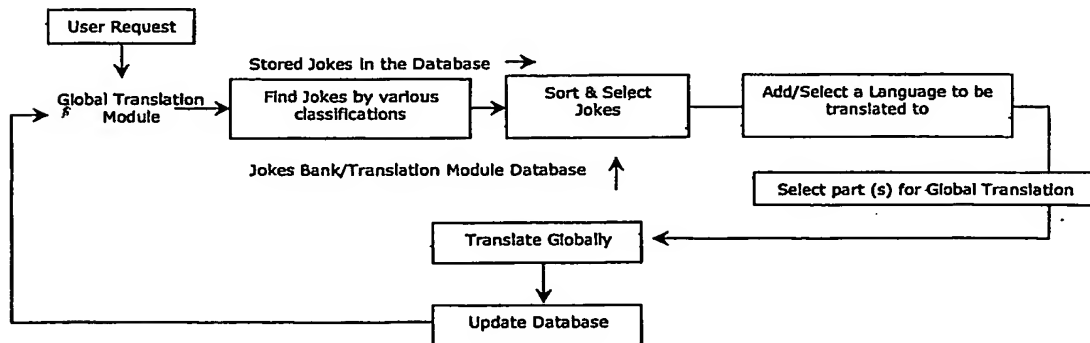


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FIG 44

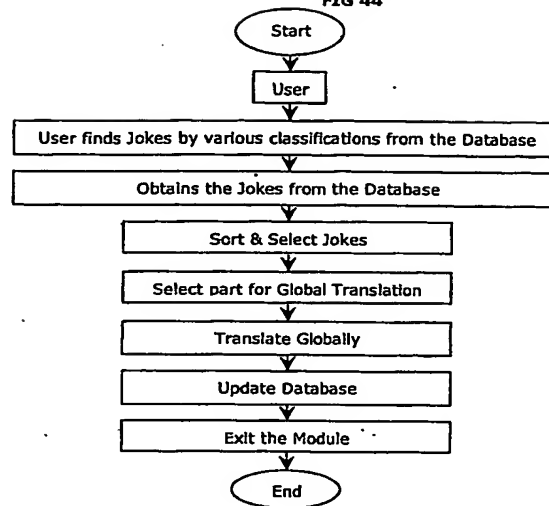
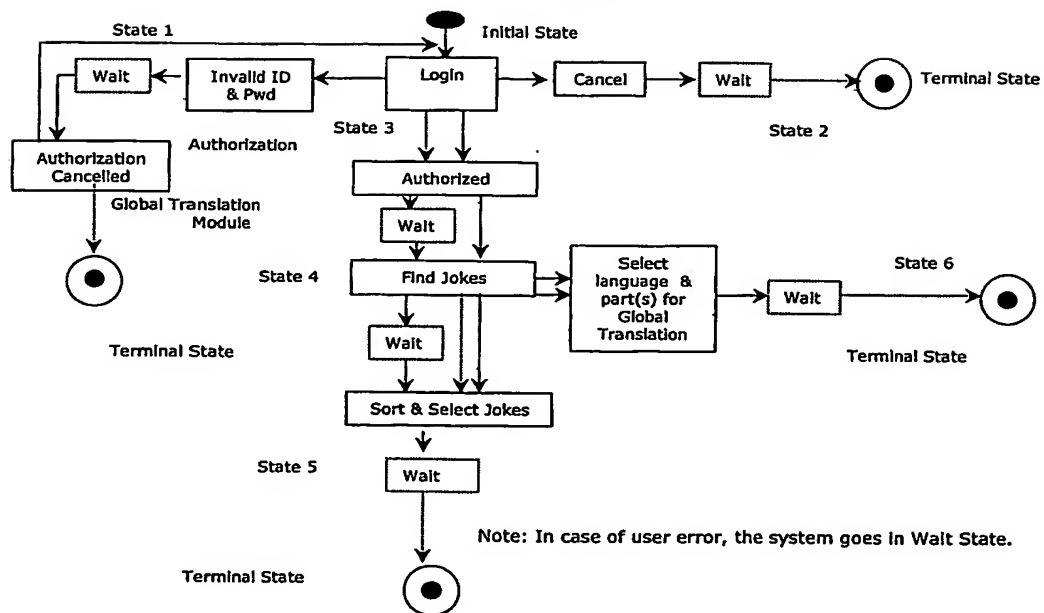


FIG 45



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FIG 46

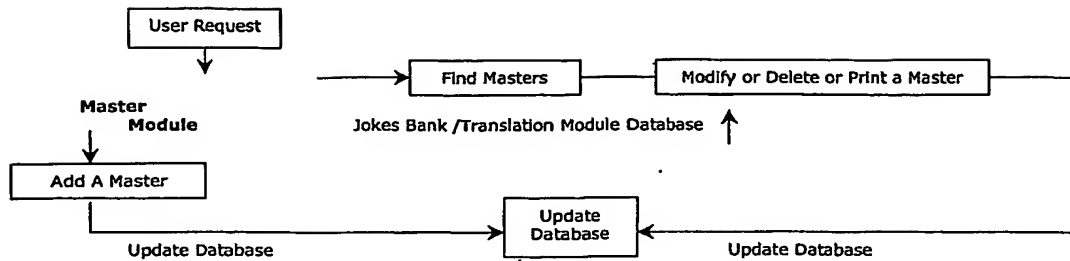
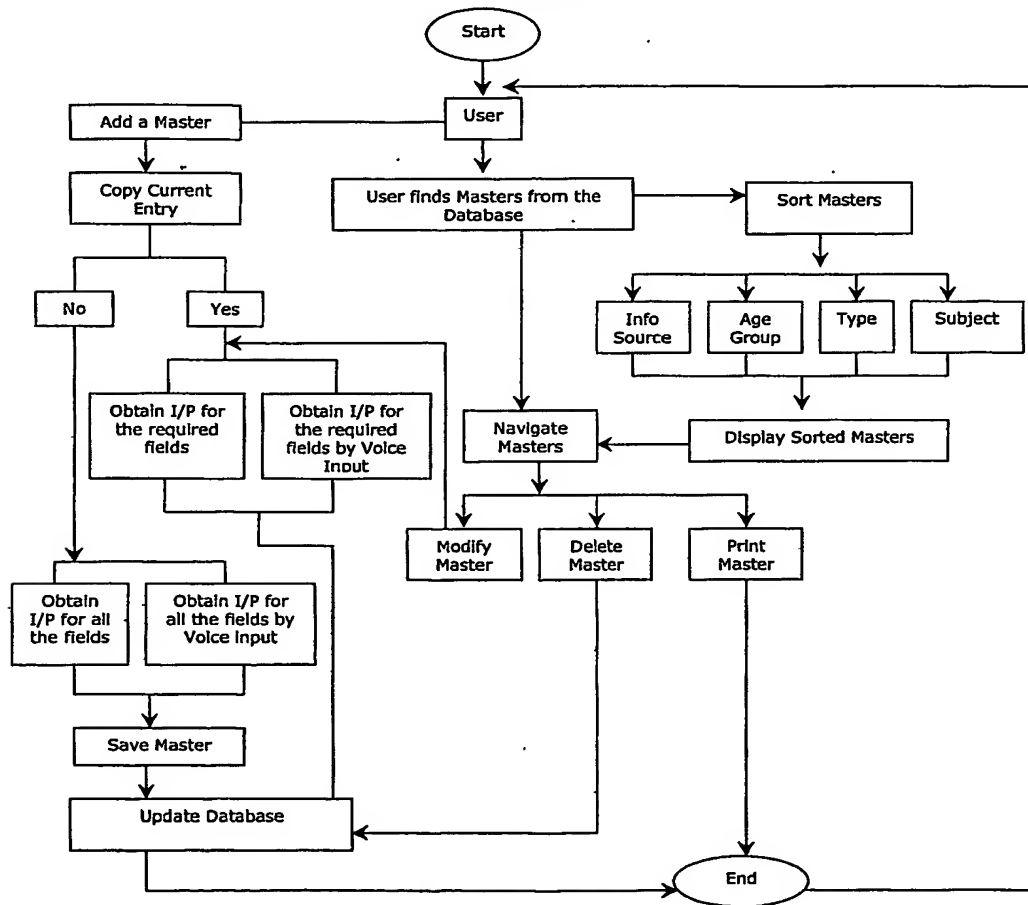
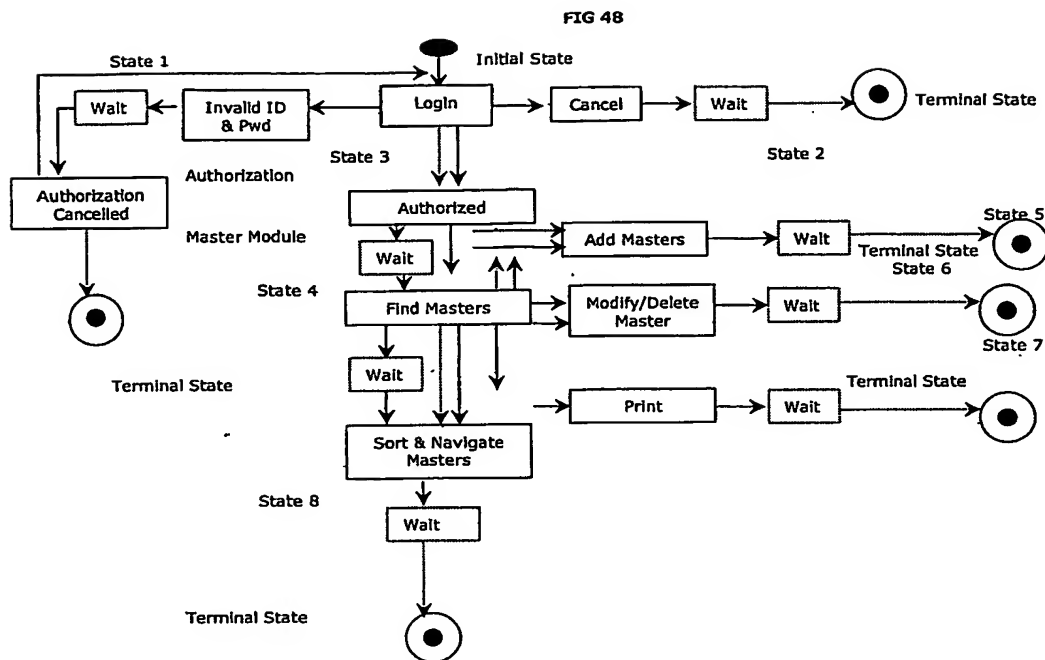


FIG 47



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Note: In case of user error, the system goes in Wait State.